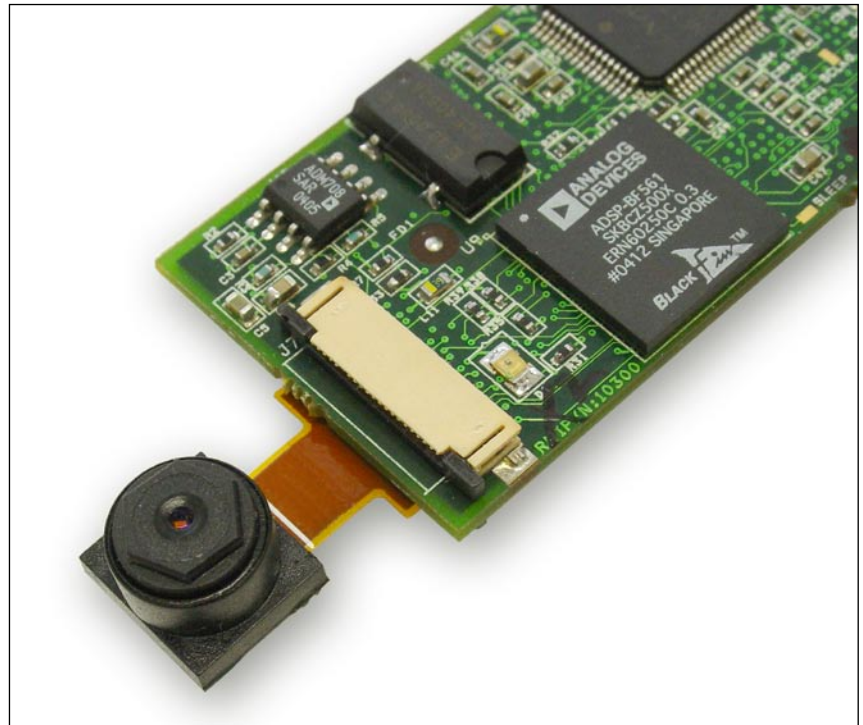


# IAD-561 Imaging Algorithm Development Kit

**For Still & Video Imaging**

- ❖ SIB-561 Interface Board
- ❖ 500 MHz, 16-Bit DSP
- ❖ VGA Resolution Camera Chip
- ❖ Example Software

**With BLACKfin™  
ADSP-BF561**

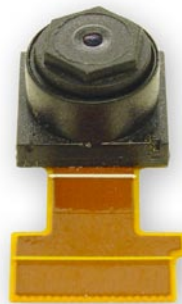


The IAD-561 Imaging Algorithm Development Kit provides an excellent platform to test and evaluate algorithms for photo/video products, security systems, portable communications gear and other applications which require high-speed data transfer in a compact package.

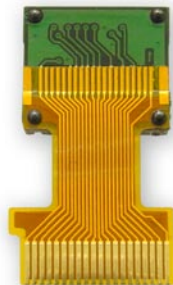
Kit contents include the RMI Products SIB-561 Interface Board, a VGA resolution camera chip and custom-designed example software. The compact SIB-561 features 64 MB data storage and a flexible USB 2.0 interface; enhanced media instructions for audio, image and video multimedia applications are built into the on-board 16-Bit DSP. The low-voltage, high-sensitivity CMOS image sensor provides all the functionality of a single chip VGA camera and image processor in one remarkably small footprint.

The package also includes RMI Products example software which enables on-screen video and still image capture. The software includes an ICE interface for debugging and is provided in source code format for easy analysis and modification of the SIB-561 firmware via the USB interface.

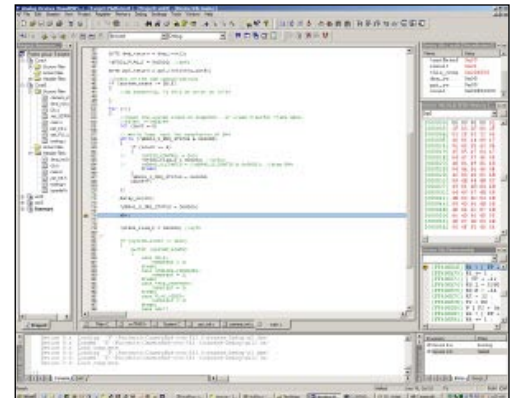
The IAD-561 Kit can also be bundled with popular Analog Devices development tools such as VisualDSP++™ and ICE Emulator™. Please contact us for details.



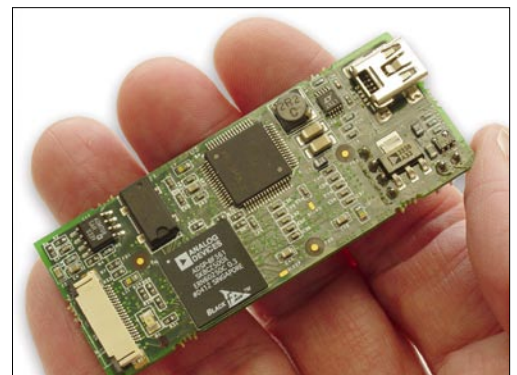
Camera chip, top view



Camera chip, bottom view



Example software is provided in source code format for easy analysis and modification.



The compact SIB-561 Interface Board measures just 1" wide x 2.5" long.